AMENDMENTS TO THE SPECIFICATION:

Please replace paragraph numbers [0018] [0019], [0020] and [0021], beginning on page 4 and ending on page 7, with the following amended paragraphs:

third electromechanical means 110 can be a plurality of electromechanical means 108, and a third electromechanical means 110 can be a plurality of electromechanical devices positioned serially along the waveguide path sufficiently adjacent to the waveguide path to change a physical dimension of the waveguide path upon actuation of at least one of the plurality of electromechanical devices. As referenced herein, an electromechanical device is positioned sufficiently adjacent to the waveguide path when it can alter a physical dimension of the waveguide path by any detectable amount. In addition, the fourth electromechanical means 112, fifth electromechanical means 114, and sixth electromechanical means 116 can be another plurality of electromechanical means positioned serially along the waveguide path sufficiently adjacent to the waveguide path to change a physical dimension of the waveguide path upon actuation of at least one of the other plurality of electromechanical devices. Each of the electromechanical means 106-116 106, 108, 110, 112, 114, 116 is one of a piezoelectric device, micro-electromechanical device, electrostatic device, or another type of electromechanical device suitable for changing a physical dimension of the waveguide path.

[0019] As shown in the exemplary Fig. 1 embodiment, a plane containing the first electromechanical means 106 and fourth electromechanical means 112 is normal to the waveguide path 104 at point 118 and the planes containing the other sets of electromechanical

means 108/114 and 110/116 are normal to the waveguide path 104 at points 120 and 122, respectively. As referenced herein, "normal" refers to being oriented relative to the path in a manner sufficient to impact the path upon actuation. Each of the electromechanical means 106-116 106, 108, 110, 112, 114, 116 respectively has a shutter 124-134 124, 126, 128, 130, 132, 134. The upper shutters 124-128 124, 126, 128 can descend toward the bottom surface 102b and the lower shutters 130-134 130, 132, 134 can ascend toward the top surface 102a. Between each of the shutters (e.g., 124 and 130) of a respective set of electromechanical means (e.g., 106 and 112) there is an opening (e.g., 136) normal to the waveguide path 104 between the shutters (e.g., 124 and 130). The height of the opening (e.g., 136) between respective shutters (e.g., 124 and 130) is dependent upon the amount of actuation that has taken place in their respective electromechanical means (e.g., 106 and 112).

[0020] As shown in Fig. 2, which is a cross-sectional view 200 along line A-A' of the exemplary embodiment 100 in Fig.1, side surfaces of the upper shutters 224-228 224, 226, 228 can be electrically connected (directly or indirectly) to the top surface 202a of the waveguide 202 with conductive means, such as spring fingers 242-252 242, 244, 246, 248, 250, 252, or any other suitable conductor or semiconductor. Side surfaces of the lower shutters 230-234 230, 232, 234 are electrically connected (directly or indirectly) to the bottom surface 202b of the waveguide 202a 202 with a set of spring fingers 254-264 254, 255, 256, 258, 260, 264. In the alternative, or in addition, electrical connection can be made with, for example, conductive brush like structures. Flexible conductive films can also be attached at

points along the sides of the shutters with enough slack in the film to allow the shutters to move up and down.

[0021] As shown in Fig. 3, which is a cross-sectional view along line B-B' of an exemplary means 110 in Fig.1, the electromechanical means is a piezoelectric device 310 having a shutter 326 that is connected to the central point 366 of a piezoelectric element 368. The ends of the piezoelectric element 368 are attached to the housing 311 of the piezoelectric device 310. The representation of the shutter 326, the central point 366 and the piezoelectric element 368 in solid lines of Fig. 3 is an illustration of an actuated state of the device (e.g., a voltage is being applied across the piezoelectric element 368 by wires at the ends of the piezoelectric element 324). The representation of the shutter 326', the central point 366' and the piezoelectric element 368' in dashed lines is an illustration of an unactuated state of the device (e.g., no voltage is being applied across the piezoelectric element 368'). The magnitude of the voltage applied to the piezoelectric element can be used to determine the amount of movement or actuation that the shutter 326 will undergo, and the final position 370 that the shutter will hold. The shutter 326 can move to, and hold, any position within a range of positions 372 depending upon the voltage applied across the piezoelectric element 324 368.

Please replace paragraph number [0023], beginning on page 7 and ending on page 8, with the following amended paragraph:

[0023] The actuation of the shutters 424-434 424, 426, 428, 430, 432, 434 into the waveguide 402 changes the physical dimensions of the waveguide path 404, as shown in Fig. 4. For example, the cross-sectional area of the waveguide path 404 at a point B in the opening Ob between the first shutter structure 424 and fourth shutter structure 430 has been reduced. Further along the waveguide path 404 at a point C in the opening Oc between the second shutter structure 426 and the fifth shutter structure 432 the cross-sectional area is further reduced. At point D along the waveguide path 404, the cross-sectional area in the opening Od between the third shutter structure 428 and fourth shutter structure 434 is the same as the cross-sectional area between the first shutter structure 424 and fourth shutter structure 430.

Please replace paragraph numbers [0025], [0026] and [0027], beginning on page 8 and ending on page 10, with the following amended paragraphs:

[0025] As shown in Fig. 4, the admittance Y Yin along the waveguide path 404 can be modeled to use impedance matching techniques of transmission line theory. Each opening Ob, Oc, and Od represents a stub in the transmission line equivalent model. The admittance Y Yin includes components Yb, Yc, Yd, of each stub each of which represents the admittance of a respective stub (i.e., set of shutters) and is a function of the cross-sectional area of an opening, and the separations L. Separations (i.e., Lbc and Lcd) between openings (i.e., Ob, Oc, and Od) affect how the reflections from these admittances Yb, Yc and Yd combine to yield the overall reflection seen at both ports of the waveguide 402. Since the separations are fixed, the combination of openings is chosen via actuation of shutters so that the desired

amount of phase shift and impedance match is achieved. For example, in Fig. 4, the combined reflection from the two outboard stubs nominally cancels the reflection from the center stub.

Symmetry of the stub arrangement reduces losses due to reflection but is not necessary.

Fig. 5 illustrates an exemplary embodiment 500 of a dynamic inline phase shifter [0026] having a waveguide 502 through which a signal travels in one of two directions (e.g. bidirectional) along the waveguide path 504. The waveguide 502 has a first (e.g., top) surface 502a and a second (e.g., bottom) surface 502b that are parallel to each other. Positioned within the waveguide 502 adjacent to and along the top surface are a first electromechanical means 506, a second electromechanical means 508, and a third electromechanical means 510. Positioned within the waveguide 502 adjacent to and along the bottom surface 502b are a fourth electromechanical means 512, a fifth electromechanical means 514, and a sixth electromechanical means 516. The first electromechanical means 406 506, second electromechanical means 508 and third electromechanical means 510 are a plurality of electromechanical means positioned serially along the waveguide path 504 sufficiently adjacent to the waveguide path 504 to change a physical dimension of the waveguide path upon actuation of at least one of the electromechanical means. In addition, the fourth electromechanical means 512, fifth electromechanical means 514, and sixth electromechanical means 516 are another plurality of electromechanical means positioned serially along the waveguide path 504 sufficiently adjacent to the waveguide path 504 to change a physical dimension of the waveguide path upon actuation of at least one of the electromechanical means. Each of the electromechanical means 506-516 <u>506, 508, 510, 512, 514, 516</u> is an array of piezoelectric devices, an array of micro-electromechanical devices, or an array of other types of electromechanical devices suitable for changing a physical dimension of the waveguide path.

[0027] As shown in Fig. 5, each of the arrays 506-516 506, 508, 510, 512, 514, 516 has first and second rows of micro-electromechanical devices, respectively shown as x and y in Fig. 5. Each of the micro-electromechanical devices in rows x and y of arrays 506-510 506, 508, 510 has a shutter 524. Each of the micro-electromechanical devices in rows x and y of arrays 512-516 512, 514, 516 has a shutter 526. The shutters 524 of arrays 506-510 506, 508, 510 can move or unroll toward the bottom surface 502b and the shutters 526 of arrays 512-516 512, 514, 516 can move or unroll toward the top surface 502a. Each of the micro-electromechanical devices in row x of arrays 506-510 506, 508, 510 is connected (directly or indirectly) to the top surface 502a of the waveguide with a conductive strip 530. Each of the micro-electromechanical devices in row x of arrays 512-516 512, 514, 516 is connected (directly or indirectly) to the bottom surface 502b of the waveguide with a conductive strip 532.

Please replace paragraph number [0029], beginning on page 10 and ending on page 11, with the following amended paragraph:

[0029] Fig. 6 is a perspective view of a first row exemplary micro-electromechanical device 600x and a second row exemplary micro-electromechanical device 600y on a dielectric

substrate 609 from the exemplary embodiment shown in Fig. 5. The micro-electromechanical devices 600x and 600y respectively include a shutter 624x and 624y mounted on the substrate 609. The shutter 624x is connected to the top or bottom surface of a waveguide (depending if it is in a top or bottom array) by the conductive film 630. The shutters 624x and 624y are respectively mounted above irises 631x and 631y in the substrate 609. Sill electrodes 632x and 632y are respectively mounted below the irises 631x and 631y in the substrate 609. A voltage applied between the <u>a</u> sill electrode 632 632x, 632y and the <u>a respective</u> shutter 624 624x, 624y of a respective device by wires provides an electrostatic force between the shutter and the sill electrode. The electrostatic force pulls the <u>a</u> shutter 624 624x, 624y down over the <u>an</u> iris 631 631x, 631y toward the <u>a</u> sill electrode 632 632x, 632y of the respective device.

Please replace paragraph numbers [0031], [0032], [0033] and [0034], beginning on page 11 and ending on page 14, with the following amended paragraphs:

[0031] The second row exemplary electromechanical device 600y, as shown in Fig. 6, is not actuated until the shutter 624x of the first row exemplary micro-electromechanical device 600x overlaps or contacts the shutter 624y of the second row exemplary micro-electromechanical device 600y. In general, a subsequent row of an array is not actuated until the row above has been fully actuated if the array is near the top surface or until the row below has been fully actuated if the array is near the bottom surface. A sill insulator can be used to prevent shorts between the sill and the shutter when a shutter is fully actuated. For example, as shown in Fig. 6, the shutter 624x of the first row exemplary micro-electromechanical

device 600x is insulated from the sill electrode 632x by a sill insulator 634x when 624x of the first row exemplary micro-electromechanical device 600x overlaps or comes into contact with the shutter 624y of the second row exemplary micro-electromechanical device 600y.

Subsequently, the shutter 624y of the second row exemplary micro-electromechanical device 600y can unroll to and hold a position within a range of positions 635 depending upon the voltage applied between the shutter element 624y of the second row exemplary micro-electromechanical device 600y and the sill electrode 632y. The second row exemplary micro-electromechanical device 600y also may include a sill insulator 634y between the sill electrode 632y and the shutter 624y.

[0032] The description of the micro-electromechanical devices 600x and 600y in Fig. 6 is for electro-mechanical devices in arrays adjacent to the top surface, such as 506-510 506, 508, 510 shown in Fig. 5. Micro-electromechanical devices for the arrays adjacent to the bottom surface, such as 512-516 512, 514, 516 shown in Fig. 5, can have the shutter mounted on the substrate below the iris in the substrate and the sill electrode mounted above the iris in the substrate. Each row of micro-mechanical devices within each array can have a sill electrode for all of the micro-mechanical devices in a row. Furthermore, the portion of a row x micro-electromechanical device having the coiled portion of shutter can protrude from a surface of the waveguide.

[0033] The embodiment in Fig. 5 can also be represented and modeled as shown in Fig. 3. For example, a first voltage applied to row x of the first array 506, the third array 510, the

fourth array 512 and the sixth array 516 that halfway closes the irises in row x of these respective arrays. The first voltage is also applied to row y of the second array 508 and the fifth array 514 so that the irises in row y of these respective arrays are halfway closed. A second voltage is applied to row x of the second array 508 and the fifth array 514 so that the irises in row y x of these respective arrays are closed. The area of the actuated positions (i.e., area of closed or partially closed iris) for the shutters in the first array 506 can be summed together along with the susceptance of the substrate (which includes any unactuated devices) that the first array 506 is on and thus be collectively seen as the first shutter structure 424 in Fig. 4. Likewise, second array 508 can be seen as the second shutter structure 426, third array 510 can be seen as the third shutter structure 428, fourth array 512 can be seen as the fourth shutter structure 430, fifth array 514 can be seen as the fifth shutter structure 432, and sixth array 516 can be seen as the sixth shutter structure 434.

[0034] To achieve a result comparable to that of the Fig. 4 embodiment, the cross-sectional area of the waveguide path 404 at a point B in the opening Ob between the first shutter structure 424 and fourth shutter structure 430 of Fig. 4 can be substantially equal (i.e., to within ten percent, or more or less) to a summation of the open irises in the first array 506, the fourth array 512, and the opening 536 534 between the first and fourth arrays. The cross-sectional area of the waveguide path 404 at point C in the opening Oc between the second shutter structure 426 and the fifth shutter structure 432 of Fig. 4 is less than the cross-sectional area of Fig. 4 the waveguide path at point B in the opening Ob, and is less because of the actuation of the shutters in both rows of the second array 508 and fifth array 514. The opening

Oc between the second shutter structure 426 and fifth shutter structure 432 can be substantially equal to a summation of the open irises in the second array 508, the fifth array 514, and the opening 536 between the first and fourth arrays. The cross-sectional area of the waveguide path 404 at point D in the opening Od between the third shutter structure 428 and sixth shutter structure 436 434 can be substantially equal to a summation of the open irises in the third array 510, the sixth array 516, and the opening 536 538 between the first and fourth arrays. Alternately Alternatively, those skilled in the art will appreciate that each set of arrays can have a unique opening size to tune the sets of arrays for impedance matching purposes. Furthermore, some or all of the arrays can have more or less than two rows of microelectromechanical devices.

Please replace paragraph number [0036], beginning on page 14 and ending on page 15, with the following amended paragraph:

[0036] Fig. 7 illustrates an exemplary radar system 700 having a plurality of dynamic inline phase shifters 701-705 701, 703, 705 connected to a radar transceiver 707. An actuator control circuit 709 is connected to the dynamic inline phase shifters 701-705 701, 703, 705 by wiring 711. The actuator control circuit controls the actuation of the electromechanical means in each of the dynamic inline phase shifters 701-705 701, 703, 705 and the phase shift of a signal traveling through a dynamic inline phase shifter. Each in line phase shifter can phase shift one of a transmitted 713 and received 715 radar signals. In addition, other types of signals, such as radio signals, can be phase shifted.